



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A++' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

TIME TABLE

M.Tech I Year II Semester (R25) Regular Examinations August – 2026
TIMINGS: 09:30 AM to 12:30 PM

DATE/DAY	POWER ELECTRONICS	COMPUTER SCIENCE AND ENGINEERING
17-08-2026 (MONDAY)	25EE2112 Advanced Power Electronics	25CS5812 Advances in Software Engineering
19-08-2026 (WEDNESDAY)	25EE2113 FACTS Controllers	25CS5813 Advanced Databases
22-08-2026 (SATURDAY)	25EE2114 AC Drives	25CS5815 Advanced Computer Networks
24-08-2026 (MONDAY)	25EE2117 Power Quality	25CS5817 Generative AI
26-08-2026 (WEDNESDAY)	25CS5822 Quantum Technologies and Applications	

Note: If any omissions or clashes in this time table may please be informed to the undersigned immediately.


CONTROLLER OF EXAMINATIONS

Controller of Examinations
Siddharth Institute of Engineering & Technology
(AUTONOMOUS),
PUTTUR - 517583, Tirupati Dist. A.P.


PRINCIPAL

PRINCIPAL
Siddharth Institute of Engineering & Technology
(AUTONOMOUS), Siddharth Nagar,
PUTTUR - 517 583, Tirupati Dist. A.P.